Press Release

austriamicrosystems releases new versions of its best-in-class process design kit for 0.35µm specialty processes

New HIT-Kit v3.80 includes high density silicon qualified libraries enabling significantly smaller die size along with performance improvements

Unterpremstaetten, Austria (February 15, 2012) – austriamicrosystems (SIX: AMS) business unit Full Service Foundry today announced the availability of a new analog/mixed signal high performance process design kit ("HIT-Kit") for its 0.35µm CMOS, High-Voltage CMOS and SiGe-BiCMOS specialty technologies.

Based on Cadence® Virtuoso® Custom IC technology, the new HIT-Kit significantly improves the time-to-market for highly competitive products in the analog intensive mixed signal arena. Supporting designers in creating their first-time-right mixed signal designs even for complex designs, this comprehensive design environment with its highly accurate simulation models and flexible SKILL-based PCells provides a proven route to silicon.

The new HIT-Kit 3.80 is available for Cadence Design Systems' Virtuoso® custom design platform IC 5.1.41 and supports the high performance 0.35µm process technologies C35 (CMOS), S35 (SiGe BiCMOS) and H35 (High-Voltage CMOS). The HIT-Kit comes complete with silicon-qualified digital, analog and RF library elements, complete sets of low voltage devices (3.3V and 5.0V) and high-voltage devices (20V, 50V and 120V devices) with various gate oxide thicknesses. The newly developed 0.35µm high density digital standard cell libraries included in this HIT-Kit have an increased gate density of approx. 23kGates/mm² and enable significantly smaller die size along with performance improvements. Fully characterized simulation models for a large set of simulators, extraction and verification run sets for both, Calibre and Assura and automatic layout device generators (PCells) are included. Features such as Safe Operating Area verification tool (SOAC) as well as a Life-Time simulation tool complete the HIT-Kit offering; hence product developers are enabled with a plug-and-play tool set which facilitates "first time right" designs.

"The release of the new HIT-Kit is the result of austriamicrosystems continuous efforts to deliver best in class design environments to our customers" states Thomas Riener, Senior Vice President and General Manager of austriamicrosystems’ Full Service Foundry business unit. "Using the newly developed high density digital library for our 0.35µm specialty processes, foundry customers benefit from significantly reduced die size of their designs along with performance improvements. Easy access to a best in class design environment enables our customers to immediately start design activities of highly complex products in the analog intensive mixed signal arena and to focus on their core competence – chip design."

The new silicon-validated ESD protection library guarantees 1 kV, 2 kV, 4 kV and 8 kV HBM (Human Body Model) ESD protection and is compliant to the MIL-883E, Method 3015.7 and JEDEC JESD22-A114B ESD standards. In C35 technology the total I/O libraries consist of more than 1800 cells supporting 3.3V and 3.3V/5V designs. The specialty High Voltage CMOS process H35 with its isolated libraries offers even more than 2400 core and periphery cells.
To meet our customer’s demands also in terms of flexibility, the HIT-Kit supports RedHat EL 3.0, EL 4.0 and Sun Solaris 8, 9 &10 operating systems. Read more details about this new HIT-Kit version on our Foundry Support Server at http://asic.austriamicrosystems.com/hitkit380.

About austriamicrosystems
austriamicrosystems’ business unit Full Service Foundry has successfully positioned itself in the analog/mixed-signal foundry market offering well-established RF CMOS, High-Voltage CMOS, BiCMOS, SiGe-BiCMOS and embedded EEPROM processes. With superior support during the design phase, high-end tools and experienced engineers, austriamicrosystems succeeds to be an attractive analog foundry partner especially for fabless design houses.
austriamicrosystems is a leading designer and manufacturer of high performance analog ICs, combining 30 years of analog design know-how with state-of-the-art manufacturing and test facilities and production partnerships. austriamicrosystems leverages its expertise in low power and high accuracy to provide industry-leading standard and customized analog products. Operating worldwide with more than 1,200 employees, austriamicrosystems focuses on the areas of power management, sensors & sensor interfaces and mobile infotainment in its markets Consumer & Communications, Industry & Medical and Automotive. Through the combination with TAOS, a world leader in advanced light sensors, austriamicrosystems has expanded its innovative sensor offering for growth markets such as mobile devices. austriamicrosystems is listed on the SIX Swiss Exchange in Zurich (ticker: AMS). For more information, please visit www.austriamicrosystems.com

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