

Altium Designer Feature Set Summary

		Core feature set	Extended feature set
DXP Platform	Software integration platform, GUI provided for all supporting editors and viewers, Design Insight for design document preview, design compiler, file management, version control interface, scripting engine	✓	✓
Schematic – Viewer	Open, view and print schematic documents and libraries	✓	✓
Schematic – Editing	All schematic document and library editing capabilities, netlist generation	✓	✓
Simulation – Mixed Signal	SPICE 3F5/XSPICE mixed-signal circuit simulation (with PSpice® compatibility)	✓	✓
Simulation – VHDL	Simulation	✓	✓
Signal Integrity – Schematic Level	Pre-layout signal integrity analysis – includes full analysis engine, uses defaults for PCB parameters	✓	✓
PCB – Viewer	Open, view and print PCB documents	✓	✓
PCB – Board Definition & Rules	Place/edit objects on mechanical layers, design rules for high speed design, user-definable layer stack, design transfer from schematic, position components	✓	✓
CAM File – Viewer	Open and import CAM and mechanical files	✓	✓
NanoBoard Support	Range of auto-configured, swappable target-FPGA and processor daughter boards (from all chip vendors) are supported plus plug-in peripheral boards for complete flexibility in system architecture, Power Monitor for FPGA designs, Nanotalk	✓	✓
FPGA Synthesis	Custom FPGA Logic Development in C, OpenBus, Schematic, VHDL and Verilog design synthesis, Custom Wishbone Interface Component	✓	✓
Soft Device JTAG Support	Live connection to soft devices such as virtual instruments and processors running inside an FPGA	✓	✓
Hard Device JTAG Support	Interactive monitoring of pin status for any JTAG device	✓	✓
FPGA Based Instruments	Pre-synthesized FPGA-ready instruments including frequency generator, logic analyzers, terminal emulator and input/output modules and Custom Instrument	✓	✓
FPGA Processor Cores	Pre-synthesized FPGA-ready TSK165, TSK51, TSK52, TSK80 and TSK3000 processor cores and library and Virtex™-4 FX, Altera® Nios® II, and Actel® CoreMP7	✓	✓
Processor Core Embedded Tools	FPGA-compliant TSK165, TSK51, TSK52, TSK80, TSK3000, Xilinx® MicroBlaze™, Altera® Nios® II, ARM7™ and PowerPC™ 405 compilers, linkers, simulators and debuggers	✓	✓
PCB – 3D Visualization Design Environment	3D Visualization gives a realistic and rendered on-the-fly view of the board, includes MCAD-ECAD support with direct linking for STEP models and real-time clearance checking, view configurations for both 2D and 3D		✓
PCB – Layout	Image clipboard support, place/edit objects on electrical layers, create footprints, place from library, Import Wizards		✓
PCB – Interactive Routing	Interactive routing (Push and Shove, Hugging and Auto-Complete modes), differential pairs, interactive/auto placement, dynamic pin/part swapping		✓
Topological Autorouting	Topological Autorouting with full layer, object and design rule support, autoroute PCB files		✓
Signal Integrity – Layout Level	Post-layout signal integrity analysis, full analysis engine with analysis support for PCB routing		✓
PCB – Manufacturing File Outputs	Multiple output publishing allows the consolidation of multiple outputs into a single media type for better data management; generate Gerber, NC Drill, ODB++ files, STEP		✓
CAM File – Editor (Gerber, ODB++)	Import CAM and mechanical files, panelize, NC route definition, DRC, export CAM and mechanical files		✓

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